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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Chang, *et al.* Docket No.: TSM03-0924
Serial No.: 10/826,956 Filed: April 15, 2004
Patent No.: 7,190,033 B2 Issue Date: March 13, 2007
For: CMOS Device and Method of Manufacture

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: March 23, 2007

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Certificate of Mailing via First Class Mail (1 page)
Request for Issuance of Certificate of Correction (1 page)
Certificate of Correction (1 original and 1 copy)
Return Postcard

Respectfully submitted,

Michelle Moran
Legal Assistant

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Tel: 972-732-1001
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**Certificate
MAR 29 2007
of Correction**

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Patentee: Chang, et al. Docket No.: TSM03-0924
Serial No.: 10/826,956 Art Unit: 2811
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Filed: April 15, 2004 Examiner: Sara W. Crane
For: CMOS Device and Method of Manufacture

Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Request for Issuance of Certificate of Correction
Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322

Dear Sir:

Upon review of the above-identified issued patent, Patentee notes errors in the claims that should be corrected as shown on the attached Certificate of Correction.

The mistakes noted on the attached Certificate of Correction were incurred through the fault of the Patent and Trademark Office. Accordingly, no fee is required.

Patentee respectfully solicits the issuance of the requested Certificate of Correction.

Respectfully submitted,

March 23, 2007
Date

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,190,033 B2
APPLICATION NO. : 0/826,956
ISSUE DATE : March 13, 2007
INVENTOR(S) : Chang, *et al.*

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, line 15; delete "en" insert --an--

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

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